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# DDDLIB: A Library For Solving Quantified Difference Inequalities

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**Abstract.** DDDLIB is a library for manipulating expressions in a first-order logic over Boolean variables and inequalities of the form  $x_1 - x_2 \leq d$ , where  $x_1, x_2$  are real variables and  $d$  is an integer constant. Expressions are represented in a semi-canonical data structure called difference decision diagrams (DDDs) which provide efficient algorithms for constructing expressions with the standard Boolean operators (conjunction, disjunction, negation, etc.), eliminating quantifiers, and deciding functional properties (satisfiability, validity and equivalence). The library is written in C and has interfaces for C++, Moscow ML and Ocaml.

## 1 Introduction

DDDLIB is a library for deciding functional properties of quantified difference inequalities which are expressions  $\varphi$  of the form

$$\varphi ::= \mathbf{0} \mid \mathbf{1} \mid b \mid x_1 - x_2 \sim d \mid \neg\varphi \mid \varphi_1 \wedge \varphi_2 \mid \varphi_1 \vee \varphi_2 \mid \varphi_1 \Rightarrow \varphi_2 \mid \exists b.\varphi \mid \exists x.\varphi,$$

where  $b$  is a Boolean variable,  $x$  is a real variable,  $d$  is an integer constant, and  $\sim \in \{\leq, <, =, \neq, >, \geq\}$  is a relational operator.  $\mathbf{0}$  and  $\mathbf{1}$  denote false and true expressions, and the symbols  $\neg$  (negation),  $\wedge$  (conjunction),  $\vee$  (disjunction),  $\Rightarrow$  (implication), and  $\exists$  (existential quantification) have their usual meaning. We denote by  $\varphi[v/v']$  the substitution of all occurrences of variable  $v'_i$  in  $\varphi$  by  $v_i$ , for  $i = 1, \dots, n$ . The problem of determining whether an expression  $\varphi$  is a tautology, denoted  $\models \varphi$ , is **PSPACE**-complete [11]. Expressions of this form occur in many areas of mathematics and computer science, some examples are logical formalisms for time, actions, events, and persistence [19, 6, 12, 3], reasoning with temporal constraints [16], and planning and scheduling [2, 9]. However, there are very few tools for performing quantifier elimination and validity checking for this logic efficiently; the primary focus has been on tools for either more expressive theories such as integers or reals with addition and order (e.g., Omega Library [10] and Redlog [7]), or less expressive theories such as quantified Boolean formulae (e.g., SATO [22] and BuDDy [15]).

This paper presents a library called DDDLIB for manipulating quantified difference inequalities. Expressions are represented in a graph data structure called DDDs [18], and the library implements a number of classical algorithms, such as Bryant's Apply algorithm [5] for combining expressions with Boolean operators, Fourier–Motzkin's method [8] for eliminating quantifiers, and Bellman–Ford's shortest-path algorithm [4] for determining satisfiability. The rest of the paper is organized as follows: Section 2 gives an overview of the Moscow ML interface, Sect. 3 is a short introduction to the implementation, and Sect. 4 presents an ML model checker for real-time systems.

## 2 Interface

This section gives an overview of the Moscow ML interface to DDDLIB (C++ and Ocaml interfaces are also available). Moscow ML is a functional programming language with good support for modeling mathematical problems. The Moscow ML interface utilizes the `Dynlib` structure in Moscow ML for dynamically linking with DDDLIB, so each Moscow ML function simply delegates the call to the corresponding C function in DDDLIB. Variables have type `var`:

```

type var
val RealVar : string -> var
val BoolVar : string -> var

```

Expressions have type `ddd` and are constructed as follows:

```

type ddd
datatype comp = EQ | NEQ | LEQ | GEQ | LE | GR
val False : ddd
val True : ddd
val BoolExpr : var -> ddd
val RealExpr : var * var * comp * int -> ddd

```

Boolean connectives and operators are defined as:

```

val Not : ddd -> ddd
val And : ddd * ddd -> ddd
val Or : ddd * ddd -> ddd
val Imp : ddd * ddd -> ddd
val Exists : var * ddd -> ddd
val Replace : ddd * var * var -> ddd

```

The following functions determine functional properties of an expression:

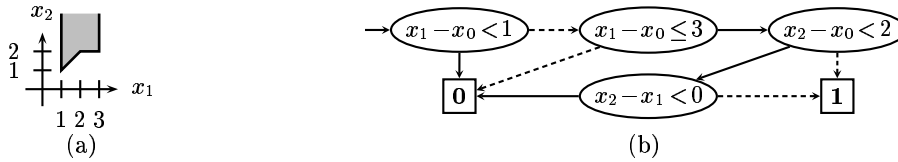
```

val Tautology : ddd -> bool
val Equivalent : ddd * ddd -> bool

```

### 3 Implementation

DDDLIB is based on (DDDS) [18] which are directed acyclic graphs with two terminal vertices, **0** and **1**, and a set of non-terminal vertices. Each non-terminal vertex  $u = \alpha \rightarrow h, l$  denotes the expression  $\varphi^u = (\alpha \wedge \varphi^h) \vee (\neg\alpha \wedge \varphi^l)$ . The test expression  $\alpha$  is a difference constraint of the form  $x_i - x_j < d$  or  $x_i - x_j \leq d$ . A Boolean variable  $b_i$  is represented as  $x_i - x'_i \leq 0$ , where  $x_i$  and  $x'_i$  are real variables used only in the encoding of  $b_i$ . Figure 1 shows an example of a DDD.



**Fig. 1.** The expression  $\varphi = (1 \leq x_1 - x_0 \leq 3) \wedge ((x_2 - x_0 \geq 2) \vee (x_2 - x_1 \geq 0))$  as (a) an  $(x_1, x_2)$ -plot for  $x_0 = 0$ , and (b) a difference decision diagram.

As shown in [18], DDDs can be ordered and path-reduced, yielding a semi-canonical form, which makes it possible to check for validity and satisfiability in constant time (as for BDDs). The DDD data structure is not canonical, however, so equivalence checking is performed as a validity check. The operations for constructing DDDs are easily defined recursively on the DDD data structure. The function  $\text{APPLY}(\oplus, u_1, u_2)$  combines two ordered DDDs rooted at  $u_1$  and  $u_2$  with a Boolean operator  $\oplus$  (e.g., negation, conjunction, disjunction).  $\text{APPLY}$  is a generalization of the version used for BDDs [5] and has running time  $O(|u_1| |u_2|)$ , where  $|\cdot|$  denotes the number of vertices in a DDD.

The function  $\text{EXISTS}(v, u)$  is used to existentially quantify the variable  $v$  in a DDD rooted at  $u$ . The algorithm is an adoption of the Fourier–Motzkin quantifier-elimination method [8], removing all vertices reachable from  $u$  containing  $v$ , while keeping all implicit constraints induced by  $v$  among the other variables, for example  $\exists x_1. (x_0 - x_1 < 1 \wedge x_1 - x_2 \leq 0) \equiv x_0 - x_2 < 1$ .

EXISTS computes modified and additional constraints in polynomial time, but has an exponential worst-case running time since the resulting DDD must be ordered. The function `PATHREDUCE(u)` removes all redundant vertices in a DDD rooted at  $u$ , making it semi-canonical: an expression  $\varphi^u$  is satisfiable if and only if `PATHREDUCE(u)  $\neq$  0`; an expression  $\varphi^u$  is a tautology if and only if `PATHREDUCE(u) = 1`. `PATHREDUCE` determines path feasibility using an incremental Bellman-Ford algorithm with dynamic programming, but has exponential worst-case running time.

## 4 Applications

In this section we show how to implement a symbolic model checker for  $\delta$ -programs using the ML interface. A  $\delta$ -program [17] is a general notation for modeling real-time systems, and consists of a set of commands of the form  $\delta \mathbf{v}.\varphi$ , where  $\mathbf{v}$  is a vector of variables, and  $\varphi$  is an expression over  $\mathbf{v}$  and  $\mathbf{v}'$ . A command  $\delta(v_1, \dots, v_n).\varphi$  nondeterministically assigns to each variable  $v_i$  any value  $v'_i$ , for  $i = 1, \dots, n$ , such that  $\varphi$  is satisfied. As we have shown in [17], an expression  $I$  holds invariantly for a  $\delta$ -program with initial state  $\varphi_0$  if and only if  $\not\models \mathbf{pre}^*(-I) \wedge \varphi_0$ , where

$$\mathbf{pre}^*(-I) = \mu X \left[ -I \vee \bigvee_{\delta \mathbf{v}.\varphi} \exists \mathbf{v}'. (\varphi \wedge X[\mathbf{v}'/\mathbf{v}]) \right], \quad (1)$$

and where  $\mu X[f(X)]$  is the least fixpoint of  $f(X)$ .

It is straightforward to model timed systems as  $\delta$ -programs. The key idea is to introduce a variable  $z$  interpreted as the common zero-point of all clocks in a timed system. A process in Fischer's protocol [13] can be modeled as follows:

$$\begin{aligned} & \delta(a_i, b_i, x_i). (\neg b_i \wedge \neg a'_i \wedge b'_i \wedge x'_i - z = 0 \wedge \bigwedge_{j=1}^N \neg id_j) \\ & \delta(a_i, b_i, x_i, id_i). (\neg a_i \wedge b_i \wedge a'_i \wedge \neg b'_i \wedge x_i - z \leq 10 \wedge x'_i - z = 0 \wedge id'_i) \\ & \delta(a_i, b_i). (a_i \wedge \neg b_i \wedge a'_i \wedge b'_i \wedge x_i - z > 10 \wedge id_i \wedge \bigwedge_{j \neq i} \neg id_j) \\ & \delta(a_i, b_i, id_i). (a_i \wedge b_i \wedge \neg a'_i \wedge \neg b'_i \wedge \neg id'_i) \\ & \delta(z). (z' \leq z \wedge (\forall z'' (z' \leq z'' \leq z) \Rightarrow \bigwedge_{i=1}^N (\neg a_i \wedge b_i \Rightarrow 0 \leq x_i - z'' \leq k))), \end{aligned}$$

where the last command is common for all processes and models the advancing of time. The initial state is given by  $\varphi_0 = \bigwedge_{i=1}^N (\neg a_i \wedge \neg b_i \wedge \neg id_i \wedge x_i - z = 0)$ , and the property that only one process is in the critical state can be expressed as  $I = \bigwedge_{i=1}^N \bigwedge_{j \neq i} \neg (a_i \wedge b_i \wedge a_j \wedge b_j)$ .

We can model a  $\delta$ -program in ML as an initial set of states `phi0` of type `ddd`, and a list of commands `cmds` of type `var list * var list * ddd` (two lists of unprimed and primed variables, and an expression). Using the functions described in Sect. 2, Fischer's protocol can be modeled with less than 25 lines of ML code. We can use Eq. (1) directly to implement a symbolic model checker for  $\delta$ -programs modeled in ML:

```
fun verify (cmds, phi0, I) =
  let val ReplaceL = ListPair.foldl (fn (v',v,d) => Replace(d,v',v))
      fun pre x = List.foldl (fn ((v,v'),d),r) =>
          Or(r, List.foldl Exists (And(d, ReplaceL r (v',v))) v')) x
      fun lfp f =
          let fun f' x =
              let val y = f x in if Equivalent(x,y) then y else f' y end
              in f' False end
          val prestar = lfp (fn x => Or(Not I, pre x cmds))
      in not (Tautology(And(phi0, prestar))) end
```

We have used `verify` to check that Fischer's protocol guarantees mutual exclusion. Within 1 hour it is possible to verify  $N = 15$  processes on a 1 GHz Pentium III PC. This is comparable with other real-time verification tools (e.g., UPPAAL [14] and KRONOS [21]). The size of `pre*(-I)` is 370,501 DDD vertices.

## 5 Conclusion

This paper has presented a library for manipulation of quantified difference inequalities implemented using a data structure called DDDs. We have demonstrated the applicability of the library in symbolic model checking by given a backward reachability algorithm as a 10-line ML function that verifies whether a  $\delta$ -program meets a given property. The library has also been used for symbolic model checking of event-recording automata [20], and verification of infinite-state systems [1]. DDDLIB is available at <http://www.it.edu/people/jm>.

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